



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : YEH
Application No. : 10/601,701
Filed : June 24, 2003
Title : METHOD FOR FABRICATION OF
POLYCRYSTALLINE SILICON THIN
FILM TRANSISTORS
Group Art Unit : 2812
Examiner : S. Isaac
Docket No. : 3230-56

MAIL STOP AF

Honorable Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL COVER SHEET

Sir:

Transmitted herewith for filing are the following:

1. AMENDMENT AFTER FINAL UNDER 37 C.F.R. § 1.116 - in response to Final Office Action of May 25, 2005.
2. PETITION FOR A TWO-MONTH EXTENSION OF TIME (in duplicate), along with authorization to **charge Deposit Account No. 501874 in the amount of \$225.00** to cover the Small Entity extension fee.
3. LETTER TO THE OFFICIAL DRAFTSPERSON, along with one (1) sheet of replacement formal drawing (Fig. 5).

The Commissioner is hereby authorized to charge any fees which may be required for the filing of this document to Deposit Account No. 501874.

Respectfully submitted,

Date: October 24, 2005

By: 

Bruce H. Troxell
Reg. No. 26,592

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LETTER TO THE OFFICIAL DRAFTSPERSON


Sir:

Enclosed is one (1) sheet of new formal drawings (FIG. 5) for the patent application noted above. Approval of the formal drawings is respectfully requested.

Respectfully submitted,

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